S No	Paper ID	TITLE	TRACK
1		Fusion using Quasilinearization technique for the Likelihood ratio based T2TA in multi Radar data fusion	ISPCC 11 (SP)
2		Biphase Amplifier Based Precision Rectifiers Using Current Conveyors	VLSI
3		Random Interleaver with Iterative IDMA Receiver for Power Line Communications	N&C
4		An Efficient Linear Regression Classifier	AI&NN
		Analysis of DelAck based TCP-NewReno with varying window size over Mobile Ad Hoc Networks	N&C
6		Improvement in Interference Rejection Capability using Space-Time Turbo Code and Adaptive Beamforming for MIMO-OFDM System	SP
7		A Rough-set Based Binarization Technique for Fingerprint Images	SP
8		OPTIMIZED WDM NETWORK WITH CONSIDERATION OF LESSER BLOCKING PROBABILITY & SHORTEST PATH SELECTION	N&C
9	115	Trajectory based methods for Nonlinear MOR: Review and Perspectives	C&A
10	116	A Gaussian Graphical Model Based approach for Image Inpainting	SP
11	120	Design of Non-Linear Adaptive Equalizer for Noise Cancellation in time-dispersed Communication Channel	SP
12	126	POLYPHASE ORTHOGONAL WAVEFORM USING MODIFIED PARTICLE SWARM OPTIMIZATION ALGORITHM FOR MIMO RADAR	SP
13	134	Classification of Liver Diseases from Ultrasound Images using Hybrid Kohonen SOM and LPND Speckle Reduction Method	SP
14	136	Exposing Structural Tampering in Digital Images	SP
15	141	Design and Analysis of Fractal Antenna for UWB Applications	N&C
16	155	Real-Time Position Estimation and Tracking of a Basketball	SP
17	164	Design and Simulation of High Level Low Power 7T SRAM Cell Using Various Process & Circuit Techniques	VLSI
18	172	power line interference cancellation in ECG signal using Alpha-Beta Filter	SP
19	176	High Speed Signed Multiplier for Digital Signal Processing Applications	SP
20	177	A Multilingual Speech Database for Speaker Recognition	SP
21	182	REDUCING DATA TRANSMISSION IN WSNs USING TIME SERIES PREDICTION	N&C
22	194	Behaviour of n-MISISFET in nanoregime-TCAD simulation	VLSI
23	202	Soft Handoff analysis and its effects on downlink capacity of 3G CDMA cellular networks	NC
24	211	Design and Implementation of Reed Solomon Decoder for 802.16 Network using FPGA	N&C
25	241	AN IMPROVED AODV ROUTING PROTOCOL FOR VANETS IN CITY SCENARIOS	N&C
26	255	DESIGN OF A CURRENT STARVED VOLTAGE CONTROLLED OSCILLATOR FOR 100-500MHz PLL	VLSI
27	256	UNSUPERVISED TECHNIQUES OF SEGMENTATION ON TEXTURE IMAGES: A COMPARISON	ISPCC 11 (SP)
28	259	Image Compression by using New Wavelet Bi-Orthogonal Filter Coefficients	ISPCC 11 (SP)

		WEIGHTED AVERAGE MULTI SEGMENT HISTOGRAM EQUALIZATION FOR CONTRAST	
29	264	ENHANCEMENT AND BRIGHTNESS PRESERVING	SP
30	269	ECG Based Human Authentication using Synthetic ECG Template	SP
31	274	A Computationally Efficient Two-Channel Quadrature Mirror Filter Bank Design Technique	ISPCC 11 (SP)
32	277	Single Image Fog Removal Using Bilateral Filter	SP
33	282	Graphical User Authentication: A Time Interval Based Approach	AI&NN
34	39	Singular Spectrum Analysis and Energy based Bearing condition monitoring	SP
35	2	Performance Optimization of FinFET for Ultralow Power Circuits	ISPCC 11 (VLSI)
36	40	CDTA Based Logarithmic Amplifier	VLSI
37	55	A review of Parametric Model Order Reduction Techiques	CA
38	67	FAR and FRR Based Analysis of Iris Recognition System	SP
39	91	Automated seizure detection in scalp EEG using multiple wavelet scales	SP
40	94	EMG Based Prediction of Elbow Motion	SP
41	100	Minimum Grounded Component Based Voltage-Mode Quadrature Oscillator using a Single Plus Type DO- DDCC	VLSI
42	105	A modified PAPR reduction scheme based on SLM and PTS Techniques	N&C
43	179	2 GHz Low Power Double Edge Triggered flip-flop in 65nm CMOS Technology	VLSI
44	198	Frequency and Voltage Scaling for Dynamic Power Reduction in 4T SRAM Cell at 45 Nm Technology	VLSI
45	206	Performance Analysis of STBC Encoded Cooperative MIMO System for Wireless Sensor Networks	ISPCC 11 (N&C)
46	223	IMPLEMENTATION OF REAL TIME SYSTEMS USING PREEMPTIVE REAL TIME KERNEL	ISPCC 11 (AI&NN)
47	232	Performance Analysis of Hybrid Robust Automatic Speech Recognition System	SP
48	234	Select-to-Spawn: A Novel Recognition-based Graphical User Authentication Scheme	AI&NN
49	253	Impact of Process Variability on Universal Gates	VLSI
50	293	Optimal Location of PMU Placement Under Contingencies Using Differential Evolution for Power System Observability	AI&NN
51	294	Performance Analysis of Double Gate MOSFETs with Different Gate Dielectric	VLSI
52	298	Delay-Distribution based Stability Analysis of Time-Delayed Port Hamiltonian Systems	C&A
53	305	Standard test bench for Optimization and Characterization of Combinational circuits	VLSI
54	313	PEER: A Protocol for Energy-Efficient Routing in Mobile Ad-hoc Networks	N&C
55	314	Content-Based Image Retrieval for Interstitial Lung Diseases	SP
56	321	FPGA Implementation of Chaotic State Sequence Generator for Secure Communication	VLSI
57	329	An Improved Approach for Control of a Grid connected Wind Farms and its STATCOM based Stability Analysis	C&A

58	330	Analytical Modelling and Staggered-Planar Structural Analysis for Organic Field Effect Transistor	VLSI
		Comparative Dynamic Analysis on Frequency Stabilization for Multi-Area power system in Deregulated	
59	335	Environment	C&A
60	79	Failure Recovery in XOR'ed Networks	N&C