

Programme Schedule IEEE PDGC-2016

Day 1 (22 December 2016)

TIME	EVENT	VENUE
8:00 AM–5:30 PM	Registration	Registration Desk
8:00 AM–10:00 AM	Breakfast	Annapurna
10:30 AM–11:15 AM	Conference Inauguration	LT-3
11:15 AM–11:45 AM	High Tea	Outside Auditorium
11:45 AM–12:45 PM	Keynote Speech by Prof. Raj K Bhatnagar, USA	LT-3
12:45 PM–2:00 PM	Parallel Technical Sessions-I	CR-5, CR-6, CR-7, CR-8
2:00 PM–3:00 PM	Conference Lunch	Annapurna
3:00 PM–4:00 PM	Keynote Speech by Prof. R. Govindarajan, India	LT-3
4:00 PM–4:15 PM	Tea Break and Networking	Rear Garden
4:15 PM–5:30 PM	Parallel Technical Sessions-II	CR-5, CR-6, CR-7, CR-8
8:00 PM–09:30 PM	Dinner	Annapurna

Day 2 (23 December 2016)

TIME	EVENT	VENUE
8:00 AM–5:30 PM	Registration	Registration Desk
8:00 AM–09:30 AM	Breakfast	Annapurna
9:30 AM–10:30 AM	Keynote Speech by Prof. Santi Caballé, Spain	LT-3
10:30 AM–11:00 AM	Tea Break and Networking	Rear Garden
11:00 AM–1:00 PM	Parallel Technical Sessions-III	CR-5, CR-6, CR-7, CR-8
1:00 PM–2:00 PM	Lunch	Annapurna
2:30 PM–3:30 PM	Keynote Speech by Prof. R.K. Ghosh, India	LT-3
3:30 PM–3:45 PM	Tea Break and Networking	Rear Garden
3:45 PM–5:30 PM	Parallel Technical Sessions-IV	CR-5, CR-6, CR-7, CR-8
8:00 PM–10:00 PM	Conference Dinner	Eutopia, Wagnaghat

Day 3 (24 December 2016)

TIME	EVENT	VENUE
8:00 AM–11:00 AM	Registration	Registration Desk
8:00 AM–09:30 AM	Breakfast	Annapurna
9:30 AM–10:30 AM	Keynote Speech by Prof. Manoj Misra, India	LT-3
10:30 AM–11:00 AM	Tea Break and Networking	Rear Garden
11:00 AM–1:00 PM	Parallel Technical Sessions-V	CR-5, CR-6, CR-7, CR-8
1:00 PM–2:30 PM	Lunch	Annapurna
3:00 PM–4:00 PM	Valedictory Function	LT3